Cisco Certified Design Expert Practical Exam

Cisco 352-011

**Version Demo** 

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### **QUESTION NO: 1**

What is a design aspect regarding multicast transport for MPLS Layer 3 VPNs using the

Rosen Draft implementation?

- **A.** LDP is the multicast control plane protocol.
- **B.** Multicast traffic is forwarded over GRE tunnels.
- C. Multicast traffic is forwarded over LDP or RSVP signaled LSPs.
- **D.** Using the MDT SAFI in BGP ensures that PIM can be disabled in the core.

## **ANSWER: B**

#### **QUESTION NO: 2**

A switched network is being designed to support a manufacturing factory. Due to cost constraints, fiber-based connectivity is not an option. Which design allows for a stable network when there is a risk of interference from the manufacturing hardware in use on the factory floor?

- A. Design the network to include UDLD to detect unidirectional links and take them out of service.
- **B.** Design the network to include Ether Channel bundles to prevent a single-link failure from taking down a switch interconnection point.
- **C.** Design the network to include loop guard to prevent a loop in the switched network when a link has too much interference.
- **D.** Design the network to include Backbone Fast on all devices to accelerate failure convergence times.

# **ANSWER: B**

#### **QUESTION NO: 3**

You are redesigning a high-speed transit network due to congestion-related issues. Which congestion avoidance mechanism can you apply to the existing network?

- A. NBAR
- B. FIFO
- C. WRED

- D. Rate-limit
- E. Policy-Based Routing

# **ANSWER: C**

# **QUESTION NO: 4**

Which two functions are performed at the distribution layer of the three-layer hierarchical network design model? (Choose two)

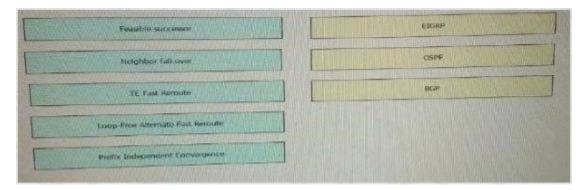
- A. Fast transport
- B. QoS classification and marking boundary
- C. Fault isolation
- D. Redundancy and load balancing
- E. Reliability

## **ANSWER: C D**

# **QUESTION NO: 5 - (DRAG DROP)**

DRAG DROP

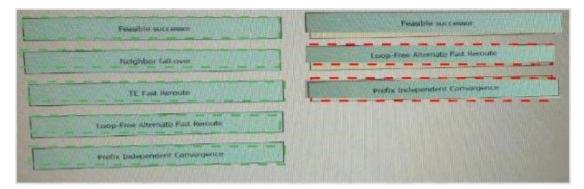
Drag the fast Reroute mechanism on the left and drop it onto the corresponding routing protocol on the right



# **ANSWER:**



# **Explanation:**



# **QUESTION NO: 6**

Which solution prevents microloops from be formed during network convergence time?

- A. RSVP-TE
- B. LFA
- C. Prefix suppression
- D. RLFA

# ANSWER: D

# **QUESTION NO: 7**

What is a design benefit of PortFast?

A. PortFast allows small, unmanaged switches to be plugged into ports of access switches without risking switch loops

- B. PortFast disables spanning-tree on the port, which puts the port into the forwarding state immediately after it is connected
- C. Portfast does not generate a spanning-tree topology change when a station on a port is connected or disconnected
- D. PortFast detects one-way communications on the physical port, when prevents switch loops
- **E.** PortFast prevents switched traffic from traversing suboptimal paths on the network F. PortFast prevents switch loops that are caused by a unidirectional point-to-point link condition on Rapid PVST+ and MST

**ANSWER: B** 

#### **QUESTION NO: 8**

Which mechanism provides fast path failure detection?

- A. Non-Stop Forwarding
- **B.** Carrier delay
- C. Graceful restart
- D. UDLD
- E. Fast hello packets
- F. iSPF

ANSWER: E

#### **QUESTION NO: 9**

Which three options are IS-IS design considerations when connecting two Layer 3 switches directly using a 10 GBASE-T cabling and formatting an IS-IS neighbor adjacency?

- A. The default IS-IS network type is point-to-point so a DIS is not elected
- **B.** A DIS is elected between the IS-IS neighbors and the elected DIS is pre-empted if router with a higher system ID is connected
- **C.** The area, levels, and interface MTU parameters must match, and system MTU must be unique for two IS-IS routers to become adjacent
- D. Faster IS-IS hello and dead timers increase bandwidth and CPU use, and may cause instability
- E. The IS-IS hello and dead timers should be tuned to detect failures as quickly as possible
- **F.** A DIS is elected between the IS-IS neighbors and the elected Dis is pre-empted if a router with a lower system ID is connected

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## **ANSWER: CDF**

## **QUESTION NO: 10**

Which two techniques are used in an OSPF network design to slow down the distribution of topology information caused by a rapidly flapping link? (Choose two)

- A. LSA throttling
- B. SPF throttling
- C. IP event dampening
- D. Link-state incremental SPF
- E. Link-state partial SPF

#### **ANSWER: A C**

# **QUESTION NO: 11**

Which two statements about VXLAN are true? (Choose two)

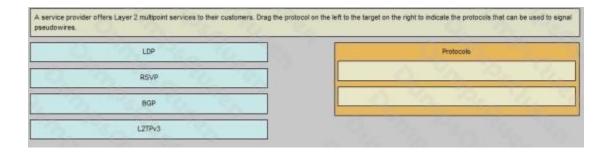
- A. VXLAN is a Cisco proprietary solution
- B. VXLAN is an encapsulation method used to create a Layer 3 overlay network
- C. VXLAN can be used to enforce Layer 2 isolation in a multitenant infrastructure
- **D.** VXLAN uses the Spanning Tree protocol for loop prevention
- E. VXLAN overcomes the 802.1Q virtual LAN address space limitation

### ANSWER: B E

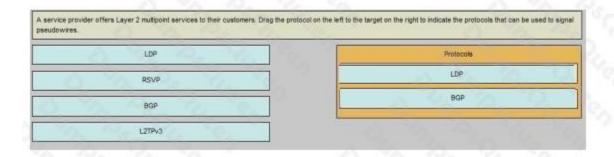
# **QUESTION NO: 12 - (DRAG DROP)**

DRAG DROP

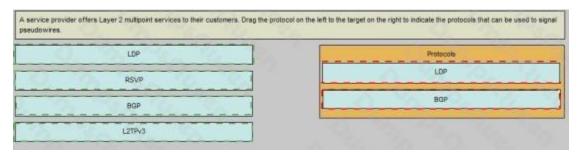




## **ANSWER:**



# **Explanation:**



## **QUESTION NO: 13**

As a part of a network design, you should tighten security to prevent man-in-the-middle. Which two security options ensure that authorized ARP responses take place according to know IP-to-MAC address mapping? (Choose two)

- A. DHCP snooping
- B. ARP spoofing
- C. ARP rate limiting
- D. Dynamic ARP Inspection
- E. Port security

ANSWER: A D	Α	N	S	Λ	Æ	R:	: <b>A</b>	D
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## **QUESTION NO: 14**

Which interconnectivity method offers the fastest convergence in the event of a unidirectional issue between three Layer 3 switches connected together with routed links in the same rack in a data center?

- A. Fiber Ethernet connectivity with UDLD enabled
- B. Copper Ethernet connectivity with BFD enabled
- C. Fiber Ethernet connectivity with BFD enabled
- D. Copper Ethernet connectivity with UDLD enabled

**ANSWER: C** 

# **QUESTION NO: 15**

Which two SAN designs appropriate to support large-scale SAN environments? (Choose two)

- A. Edge-core-edge design
- B. Fibre Channel forwarder
- C. Split fabric design
- D. Core-edge design
- E. Dual fabric design

**ANSWER: A D**