DUMPSQUEEN

Nokia Cloud Packet Core

Nokia 4A0-M05

Version Demo

Total Demo Questions: 10

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QUESTION NO: 1

Based on the exhibit output, how does a VM associated with security group "SG_CMM" handle TCP traffic?

```
[root@controller01 ~(keystone_admin)]# openstack security group rule list SG_CMM
+--------------------------------------+-------------+-----------+------------+-----------------------+
| ID                                   | IP Protocol | IP Range  | Port Range | Remote Security Group |
+--------------------------------------+-------------+-----------+------------+-----------------------+
| 0a4f49f7-a40e-4136-9376-8dabc374b032 | udp         | 0.0.0.0/0 | 161:161    | None                  |
| 80829ace-80d0-4ef8-937d-99a174a80ff0 | tcp         | 0.0.0.0/0 | 443:443    | None                  |
| 846de90b-41ff-46f9-886b-3b11acd2b36c | None        | None      |            | None                  |
| be2e9e4b-554d-4d0a-8bc7-5e4022703764 | tcp         | 0.0.0.0/0 | 123:123    | None                  |
| ded87d0a-b6ad-45e7-9726-46935254434e | tcp         | 0.0.0.0/0 | 830:830    | None                  |
| fae67809-0528-40f6-b872-80e86378b458 | tcp         | 0.0.0.0/0 | 22:22      | None                  |
+--------------------------------------+-------------+-----------+------------+-----------------------+
```

A. Drops TCP traffic with destination port 22

B. Drops TCP traffic with source port 22

C. Allows TCP traffic with destination port 22
Allows TCP traffic with source port 22

ANSWER: C

QUESTION NO: 2

The following statements describe requirements to configure CPU pinning in OpenStack. Which statement is FALSE?

A. The user specifies the compute node to be used for the VM to ensure that it supports CPU pinning.

B. A host aggregate is configured to specify the compute nodes to be used for CPU pinning.

C. A flavor is defined that supports CPU pinning.

D. Compute nodes must be configured so that Linux processes do not run on cores reserved for VMs.

ANSWER: D

QUESTION NO: 3

Which of the following best describes the reason for CPU pinning?

A. To ensure that a VM is assigned to high performance CPU cores.

B. To ensure that a VM's CPU cores area all allocated on the same NUMA node.

C. To ensure that a VM is assigned to a compute node with adequate resources.

D. To ensure that a VM is assigned a sufficient number of CPU cores.

ANSWER: A

QUESTION NO: 4

Which of the following statements regarding CMM internal network is FALSE?

A. It is a layer 2 network.

B. It is connected to all CMM VMs.

C. It is used for communication between CMM VMs.

D. It requires a customer provided subnet address.

ANSWER: D

QUESTION NO: 5

Which of the following is NOT a capability of Open vSwitch?

A. VLAN, VXLAN and GRE encapsulation

B. Layer 3 forwarding

C. Quality of Service

D. Direct pass-through from VM to the NIC

ANSWER: D

QUESTION NO: 6

Which of the following statements about the network service platform (NSP) is FALSE?

A. Acts as an element management system (EMS) for CPC components

B. Performs fault, performance, and security management

C. Has a northbound interface with the NFV orchestrator (NFVO)

D. Allows monitoring events associated with the NFV environment

ANSWER: C

Which of the following is NOT a component of the CMM virtual network function?

A. Load Balancer (LB)

B. Packet Processing Service (PAPS)

C. Network Element Cluster Controller (NECC)

D. Database Service (DBS)

ANSWER: A

Which of the following statements regarding CMM deployment is FALSE?

A. Hyperthreading and CPU pinning are enabled.

B. An anti-affinity rule is used to ensure NECC VMs are deployed on different servers.

C. Two availability zones are required for a successful CMM deployment.

D. SR-IOV and DPDK are used to accelerate packet processing.

ANSWER: D

Which of the following statements about 5G deployment option 3 is FALSE?

A. Core network may be EPC or next-generation core

B. There is a data plane interface between eNB and EPC

C. eNB has both control plane and data plane connections to the core

D. There is an X2 interface between eNB and gNB

ANSWER: B

QUESTION NO: 10

Which of the following about CMG CUPS architecture is FALSE?

A. CMG user plane VNF implements the SGW-U, PGW-U, and GGSN-U functionalities

B. A 7750 SR MG communicates with a CMG control plane VNF over Sx interface

C. CMG control plane VNF can be deployed in LB-mode or LB-less mode

D. CMG user plane VNF can be deployed in LB-mode or LB-less mode

ANSWER: C